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Amendments to the Claims

This listing of claims will replace all prior versions, and listing, of claims in the application.

1. (Currently Amended) A power supply, comprising:  
a mode setting unit for outputting a control signal ~~according~~corresponding to a display mode;  
an inverter control unit for selectively outputting a timing signal ~~received from the outside~~ according to the control signal from the mode setting unit; and  
an inverter which is operated in a synchronous mode ~~when~~upon receiving the timing signal ~~is received~~ from the inverter control unit, and in an asynchronous mode when the timing signal is not received ~~from the inverter control unit~~, wherein the inverter is operated at a frequency that is synchronized with a frequency of the timing signal during the synchronous mode and is not synchronized with the frequency of the timing signal during the asynchronous mode.
2. (Original) The power supply as claimed in claim 1, wherein the timing signal is a gate select signal or data clock signal.
3. (Original) The power supply as claimed in claim 1, wherein the timing signal is a vertical or horizontal synchronous signal.
4. (Currently Amended) A liquid crystal display device, comprising:  
a liquid crystal module including a liquid crystal panel, a gate driving unit for delivering scanning signals to the liquid crystal panel, and a data driving unit for delivering image signals to the liquid crystal panel;  
a timing controller for providing a timing signal used to control display of an image in the liquid crystal module;  
a mode setting unit for outputting a control signal ~~according~~corresponding to a display mode;  
an inverter control unit for selectively outputting the timing signal received from the timing controller according to the control signal from the mode setting unit;  
an inverter which is operated in a synchronous mode ~~when~~upon receiving the timing

signal is ~~received~~ from the inverter control unit, and in an asynchronous mode when the timing signal is not received ~~from the inverter control unit~~; and

a lamp which is operated by a voltage applied to the lamp according to the operation mode of the inverter, wherein:

a frequency of the voltage applied to the lamp is synchronized with a frequency of the timing signal during the synchronous mode and is not synchronized with the frequency of the timing signal during the asynchronous mode.

5. (Original) The liquid crystal display device as claimed in claim 4, wherein the timing signal is a gate select signal or data clock signal.

6. (Original) The liquid crystal display device as claimed in claim 4, wherein the timing signal is a vertical or horizontal synchronous signal.

7. (Original) The liquid crystal display device as claimed in claim 4, wherein the mode setting unit is included in the timing controller.

8. (Currently Amended) A method of driving a liquid crystal display device to display an image, comprising the steps of:

- (a) outputting a control signal ~~according~~corresponding to a display mode;
- (b) selectively outputting, by an inverter control unit, according to the control signal, a timing signal received from the outside; and
- (c) driving a lamp with a voltage applied to the lamp by an inverter, according to an operation mode of the inverter, the operation mode including a synchronous mode and an asynchronous mode, wherein:

a frequency of the voltage applied to the lamp is synchronized with a frequency of the timing signal during the synchronous mode in which the timing signal is ~~input~~output from the inverter control unit, and

the frequency of the voltage applied to the lamp is not synchronized with the frequency of the timing signal during the asynchronous mode in which the timing signal is not ~~input~~output from the inverter control unit.

9. (Canceled)

10. (Previously Presented) The method as claimed in claim 8, wherein step (a) comprises the steps of outputting a first level control signal when the display mode is the moving-image mode, or outputting a second level control signal when the display mode is the still-image mode.

11. (Previously Presented) The method as claimed in claim 10, wherein step (b) comprises the steps of outputting the timing signal received from the outside when the second level control signal is applied, or not outputting the timing signal received from the outside when the first level control signal is applied.

12. (Original) The method as claimed in claim 8, wherein the timing signal is a gate select signal 2 or data clock signal.

13. (Original) The method as claimed in claim 8, wherein the timing signal is a vertical or horizontal synchronous signal.

14. (Previously Presented) The power supply as claimed in claim 1, wherein the display mode is either a moving-image or a still-image mode.

15. (Previously Presented) The liquid crystal display device as claimed in claim 4, wherein the display mode is either a moving-image or a still-image mode.

16. (Previously Presented) The liquid crystal display device as claimed in claim 15, wherein the operation of the lamp is synchronized with the timing signal when the display mode is the still-image mode.

17. (Previously Presented) The method as claimed in claim 8, wherein the display mode is either moving-image or still-image mode.